

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,102,200 B2
APPLICATION NO. : 09/944222
DATED : September 5, 2006
INVENTOR(S) : Yongping Fan et al.

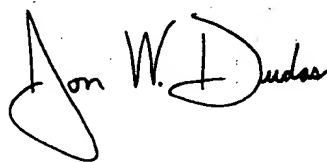
Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

| <u>Column</u> | <u>Line</u> | |
|---------------|-------------|--|
| 5 | 27 | After "connected" insert --to--. |
| 6 | 36 | Before "first transistor" insert --of the--. |

Signed and Sealed this

Twenty-seventh Day of March, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office